NNSLD PMC Pinout Table BD CAMAC / QXR Version

PMC J4 Pin	Signal	I/O
Number	Name	Direction
1	MDAT Data	In
2	MDAT Clock	In
3	(not used)	In
4	(not used)	In
5	QXR ATT2	Out
6	Fan Status	In
7	GND	
8	GND	
9	TCLK	In
10	TCLK Carrier Detect	In
11	(not used)	Out
12	PIOR Clock	In
13	PIOR Data	In
14	BTR Data	In
15	BTR Clock	In
16	QXR 5760Hz Interrupt	In
17	PIOX	Out
18	(not used)	Out
19	(not used)	Out
20	QXR ATT0	Out
21	QXR ATT1	Out
22	QXR Event 37 Pulse	Out
23	(not used)	Out
24	(not used)	Out
25	(reserved)	
26	(reserved)	
27	(reserved)	
28	(reserved)	
29	+12VDC to transition card	
30	-12VDC to transition card	
31	GND	
32	(no connection)	
33	GND	
34	GND	
39	GND	
40	GND	
61	+12VDC to transition card	
62	-12VDC to transition card	
63	+5VDC to transition card	
64	+5VDC to transition card	

Notes:

- 1. Supply voltages must be fused and filtered at the transition card.
- 2. +5VDC for the transition card is best drawn from VME P2
- 3. All inputs are TTL levels.
- 4. All outputs are TTL levels, 24 mA sink, 3 mA source